

Accelerating Semiconductor Innovation – The InnoFAB Model for Bridging Research and Manufacturing

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WORKSHOP

Advanced characterisation and technology services for semiconductor industries and quantum technologies

June 11, 2026
ALBA Synchrotron

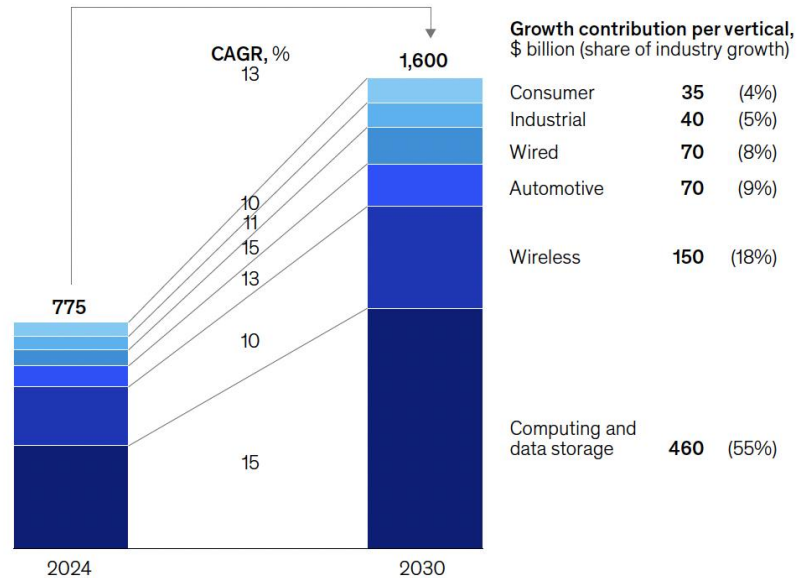


/ The semiconductor global race

Source: *Hiding in plain sight: The underestimated size of the semiconductor industry*, McKinsey (2026)

The semiconductor market could reach a value of **\$1.6 trillion** by 2030.

Global semiconductor
market (base case),
\$ billion



Source: SEMI World Fab Forecast Q3 -25

Investments in New Fabs Support AI Boom

107 New Fabs Coming Online through 2028 – **91** in progress

30+ Additional fabs still needed through 2030



- Europe needs to find its own pace in the race, balancing short-term urgencies and long-term strategic decisions

/ Europe's reality

Source: SEMI (2025)

SEMICON[®] EUROPA 50 YEARS

Europe's Microelectronics Ecosystem

European Excellence



Semiconductor Research

Semiconductor for Automotive, IOT, MS/ RF, Sensors, Power



Lithography Equipment & Materials

Mobile Network Equipment



RTOS

umec, c22, leti

SUPPLIERS

Fraunhofer, VTT, Besi, c-met, DAS, EDWARDS, EVG, EVATEC, OKMETIC, OVIVO, GW, INFICON, Infineon, amr, OSRAM, intel, Melexis, nexperia, NXP, RECIF, SEMILAB, SYNOPSIS, Schneider Electric, siltronic, soitec, SPTS, SUSS, TRUMPF, VAT, ZEISS, tsmc, SIEMENS, Mentor Graphics, ST, IIVI, xfab

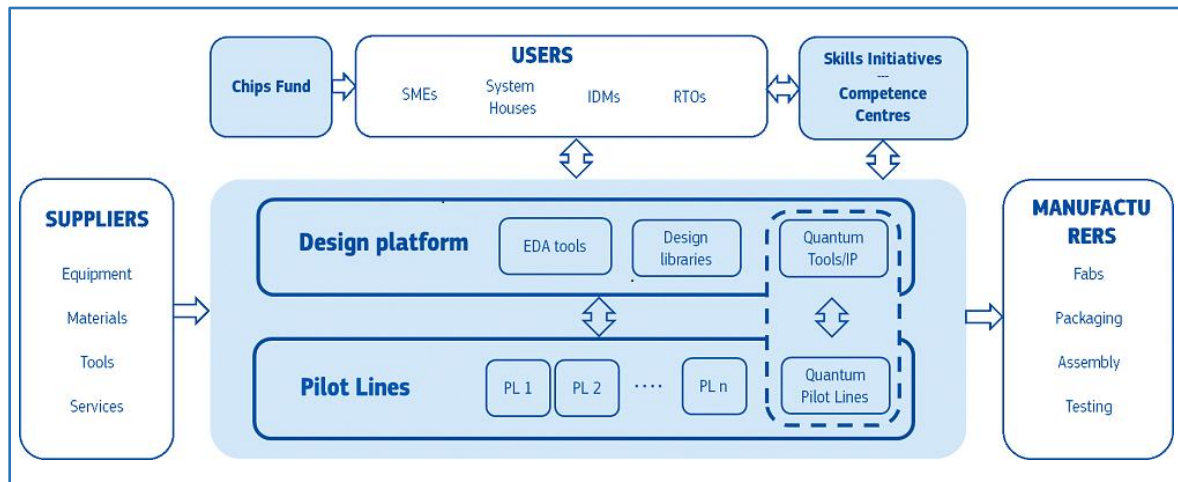
DESIGN & MANUFACTURING

arm, BOSCH, cadence, GlobalFoundries

/ Europe's strategy

Chips Act

European Chips Act: The Chips for Europe Initiative



What Chips JU Enables

Pilot lines

Prototyping of validated designs
Testing of equipment
Validation of process flows



Competence centres

Access to technical expertise, helping companies to approach and improve design capabilities and developing skills



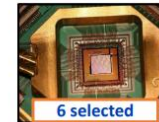
Design Platform

Help designing semiconductor devices, via access to Electronic Design Automation tools and IP libraries



Quantum chips

Technology and engineering capacities for accelerating innovative development of quantum chips

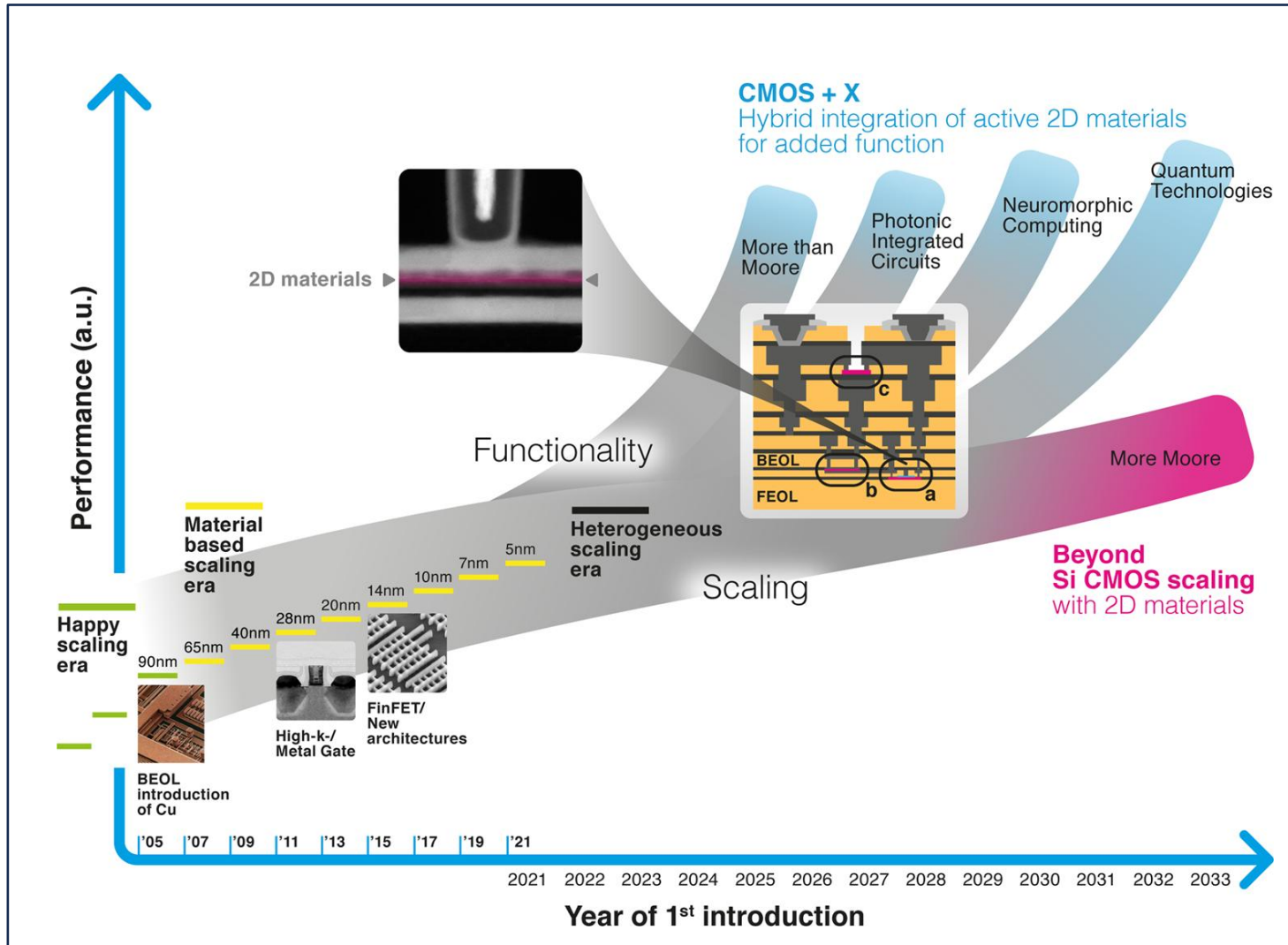


Chips Act 2.0

3. RESULTS OF EX-POST EVALUATIONS, STAKEHOLDER CONSULTATIONS AND IMPACT ASSESSMENTS

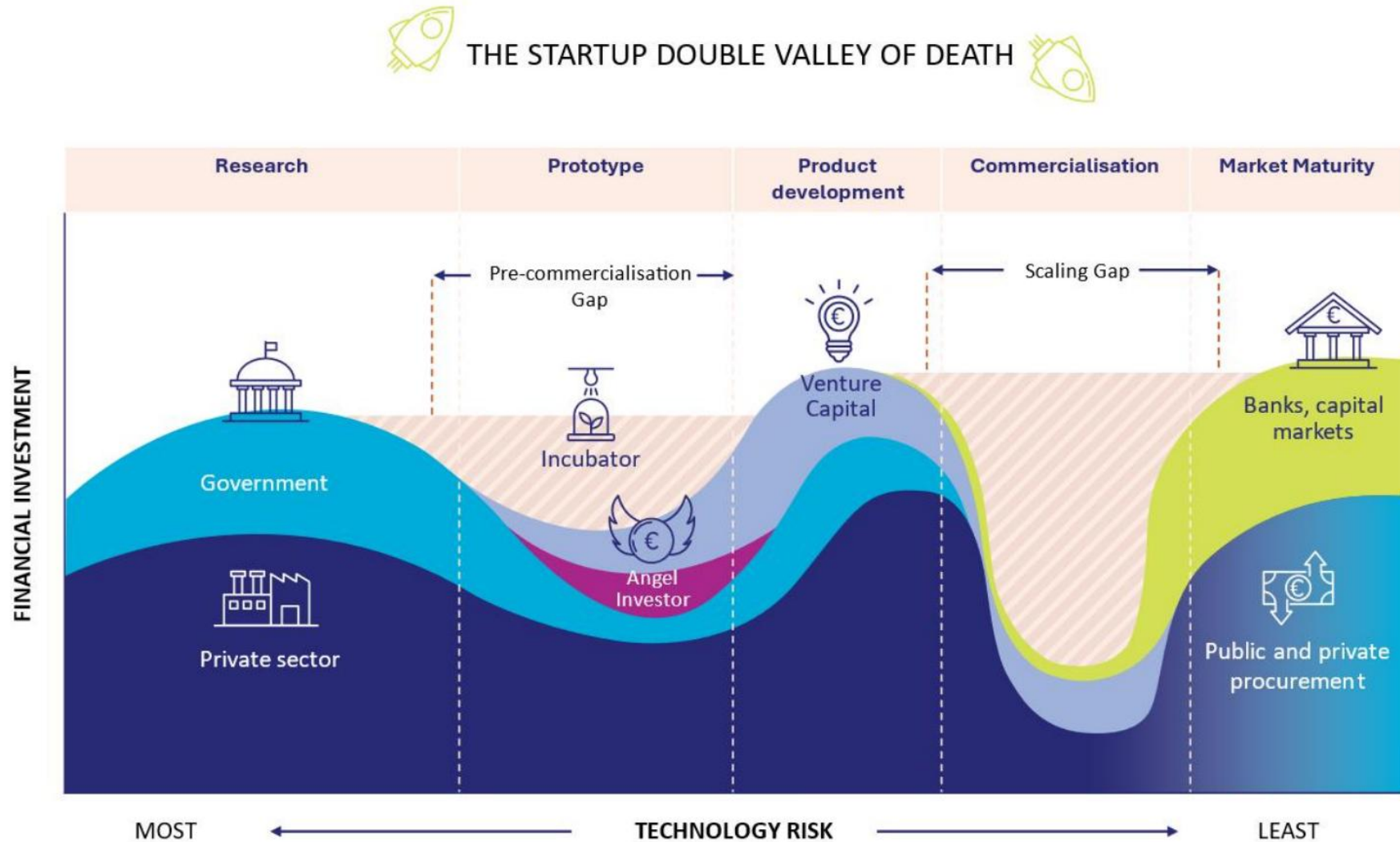
“Across the evaluation criteria, the lab-to-fab gap emerges as a major challenge”

/ Disruptive (semiconductor) innovation “made in Europe”



- Disruptive technologies can unlock unexpected capabilities and create entirely new markets.
- They offer Europe a strategic opportunity beyond the conventional More Moore industrial race.
- Europe's strength in knowledge creation can become a competitive advantage if translated into scalable technologies.

/ Disruptive technologies' double valley of death



/ The gap for semiconductor start-ups



Cleanrooms enabling breakthrough innovation in **advanced materials**



Facilities bridging the gap from research to development through **industrialization capabilities**



Incorporating **new business models** supporting start-ups

Cleanroom infrastructure is needed to support prototyping, scaling, and small/medium-series manufacturing, bridging early industrialization before large-foundry access.

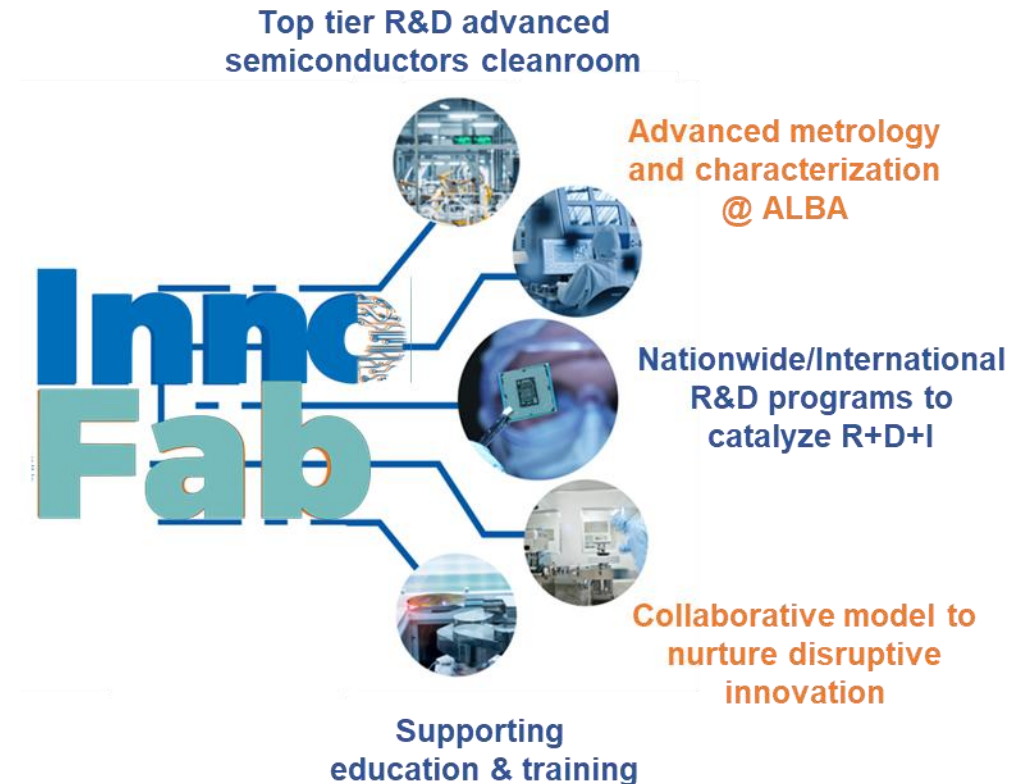
/ InnoFAB's value proposition: *bridging the Lab-to-Fab gap*

A R&D center (public foundation) for research and disruptive innovation in next-generation semiconductor technologies

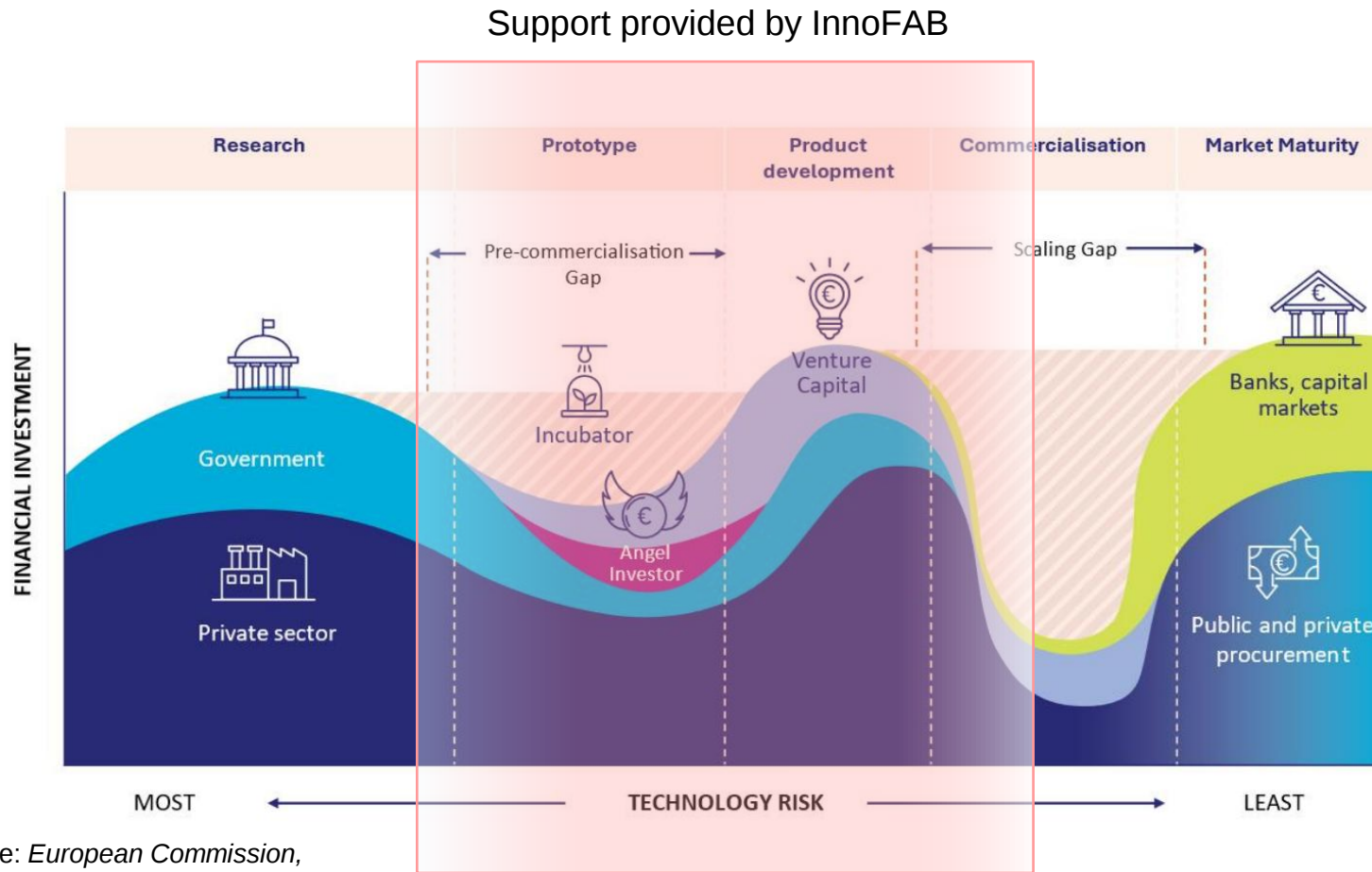


Strategic objectives:

- ❑ Boost R+D+I semiconductors ecosystem with **state-of-the-art R&D cleanroom** for innovative technologies & **advanced semiconductors** in **industry-relevant environment**
- ❑ Facilitate **Lab-to-Fab transition** and **industrialization** with flexible collaboration models for academia, start-ups, scale-ups, and industry.
- ❑ Support **education and training** for all professionals in the business, creating **highly qualified talent at all levels**.



/ InnoFAB will accelerate technology maturation



- Industrial fab infrastructure enables start-ups to mature technologies from lab demonstrators to scalable, high-yield processes.
- Shared access reduces prohibitive early-stage CAPEX and provides an economically efficient pathway for technology maturation & scale-up.
- Validated prototypes can transition toward high-volume manufacturing in existing or newly created foundries and IDMs.

Source: European Commission,
The EU Startup and Scaleup Strategy,
COM(2025) 270 final

/ Infrastructure's guiding principles



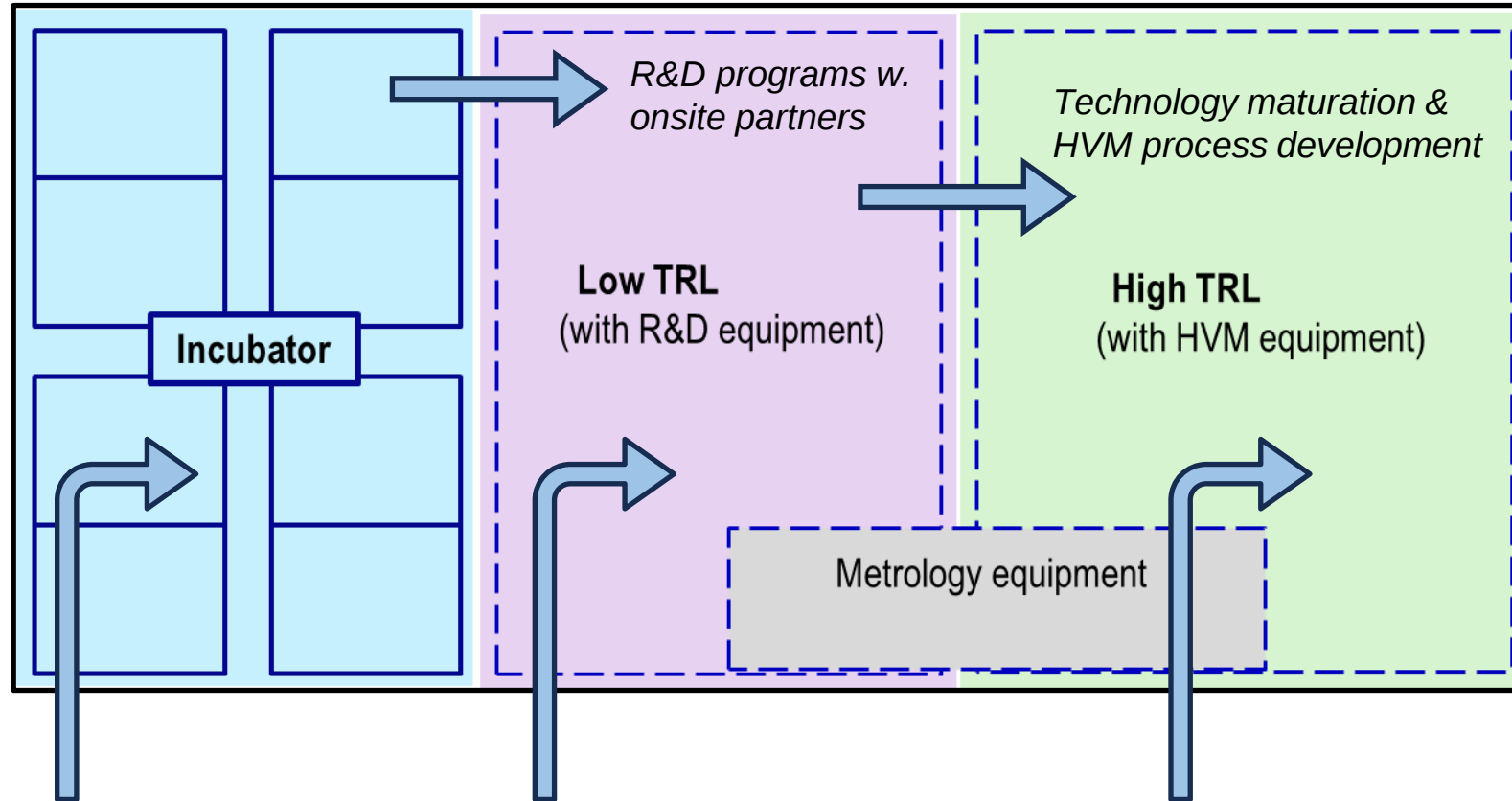
Concept for **InnoFAB** cleanroom:

4-levels *FAB* building

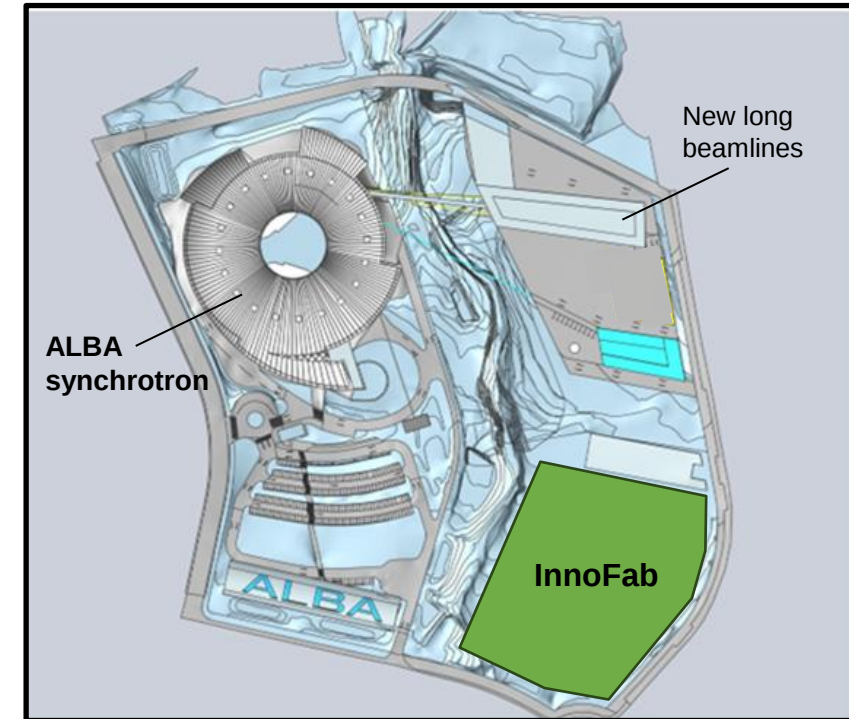
- **HVM-ready 200mm infrastructure**, with three main areas:
 - i) **Low TRL** (R&D equipment), ii) **High TRL** (HVM equipment), and iii) **Incubator** for external partners.
- **300 mm-ready-building**. Cleanroom building and HVM equipment will be “300-mm ready”, in a state-of-the-art 4-level fab-building.
- **Synergy with ALBA’s advanced characterization facility**. Colocalization of **InnoFAB** and ALBA will facilitate access to metrology, accelerating innovation cycle of next generation semiconductors technology.

/ Top-tier R&D advanced semiconductor cleanroom > 2500 m² total area

Cleanroom area of 2,500 m² integrated in >16,000 m² FAB building



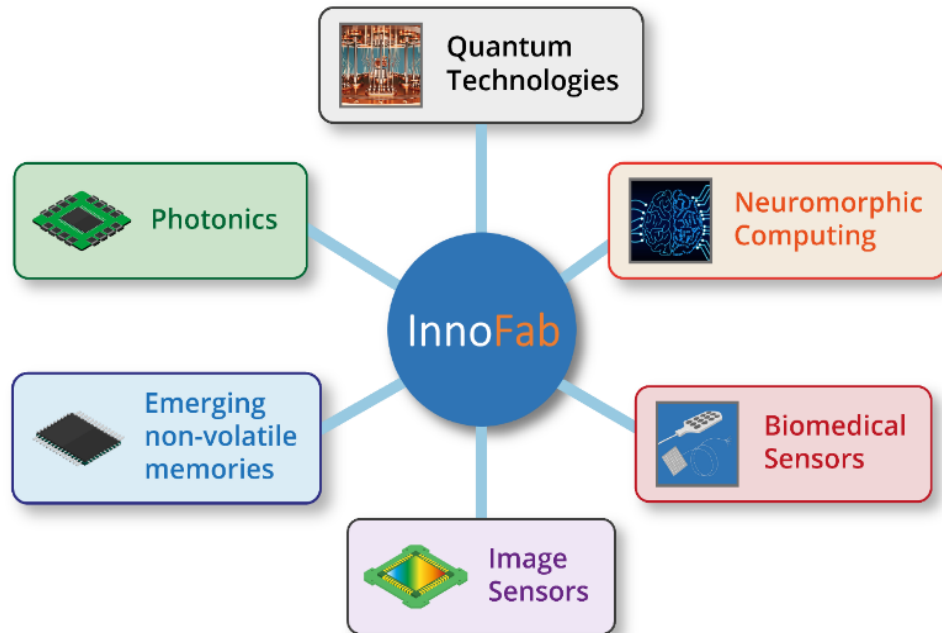
Unique synergies with ALBA synchrotron



Premium access to leading-edge materials & devices metrology/characterization

/ Technology and Applications focus

InnoFAB will support development and innovation in technologies with expected market entry > 5 - 10 years



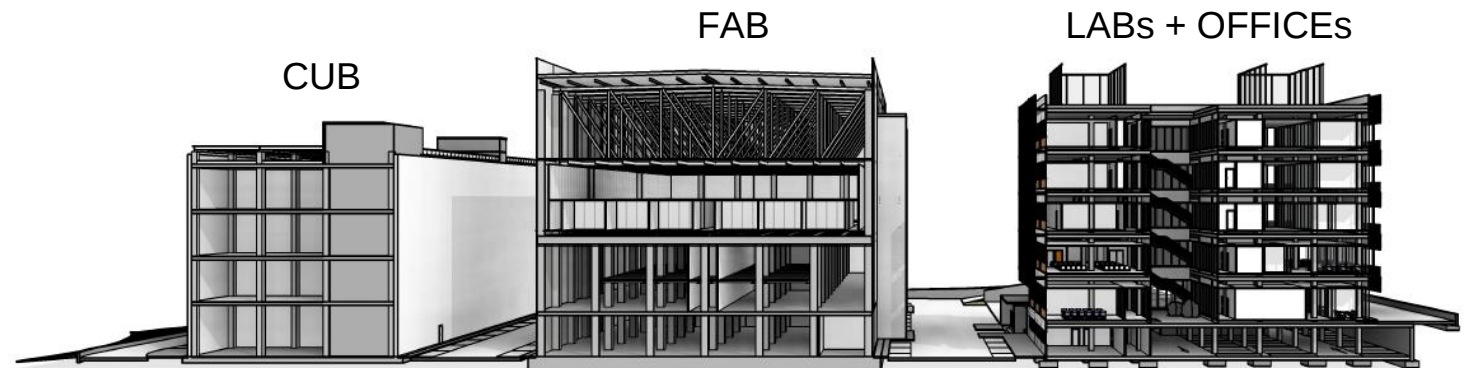
- **Beyond-CMOS application areas**

Neuromorphic computing, quantum technology, advanced photonics, emerging non-volatile memories, sensors, etc.

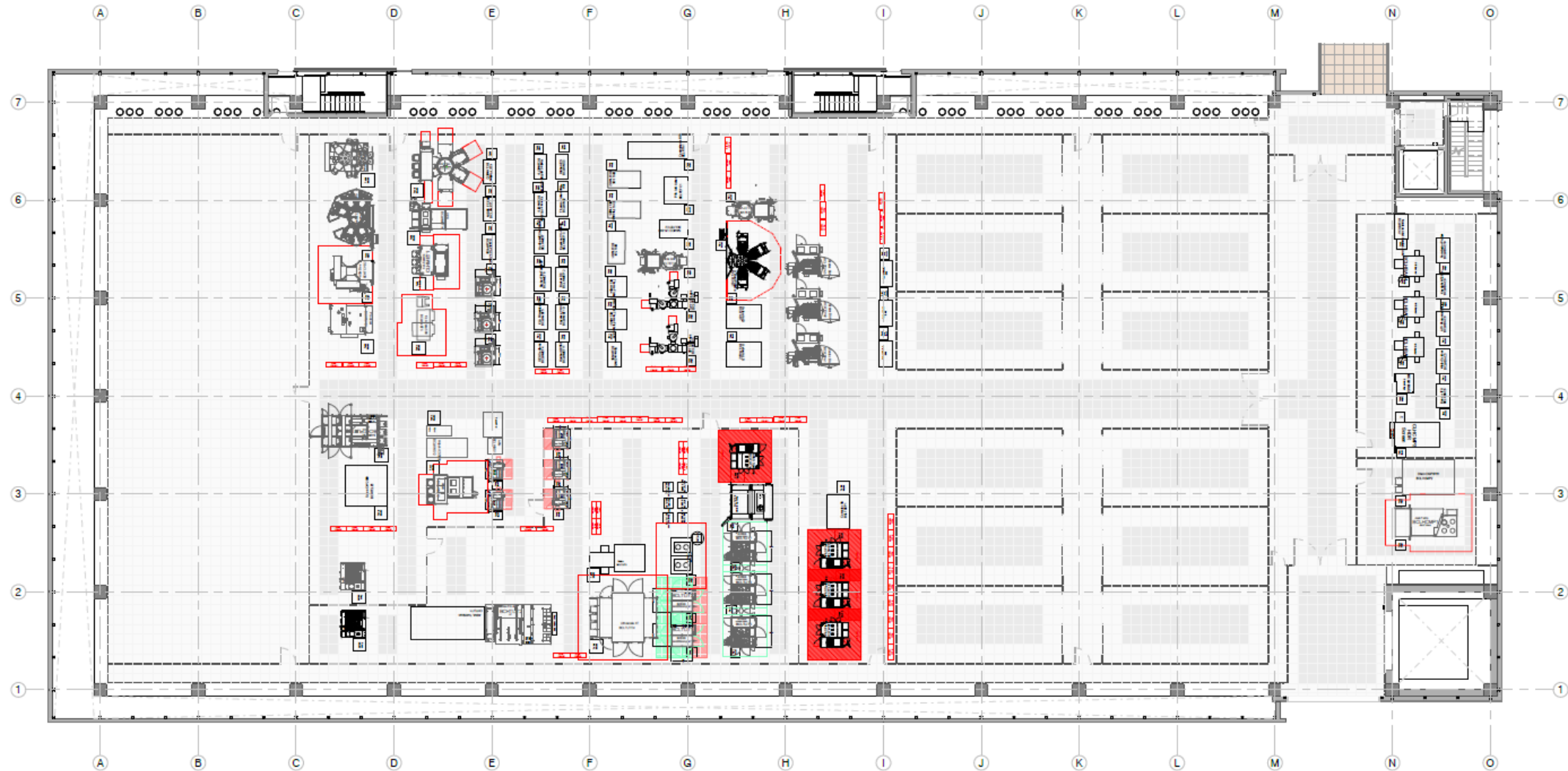
- **Leading-edge / advanced semiconductors and materials**

2D layered materials (graphene, TMDs), thin film oxides (metal oxides, ferroelectric oxides), thin film superconducting materials

/ Basic engineering project concluded



/ Basic engineering project concluded: cleanroom design (with tools) available



Nivel Sala Blanca / Clean Room Level

1 : 150

/ Economics' summary & timelines

- Total investment is **~400 M€**
(incl. cleanroom tools)



**Generalitat
de Catalunya**



- **~ 200 FTEs** at full operations
- **Timelines:**
 - **User and partner agreements** from **Q4 2026**
 - **Construction** starts **Q1 2027**
 - **Initial operations** in **2029**



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/ A Network of **National & International Chapters**



- Chapters are key stakeholders of InnoFab, all will contribute to the technology and innovation pipeline of InnoFab.
- Chapters will have synergistic and/or complementary capacities to those of InnoFab, mostly working in lower TRLs, requiring the services of InnoFab to advance in their research
- Collaboration will be in the form of preestablished, long-term collaborative research programs.

/ InnoFab Ecosystem

Main stakeholders and National Chapters

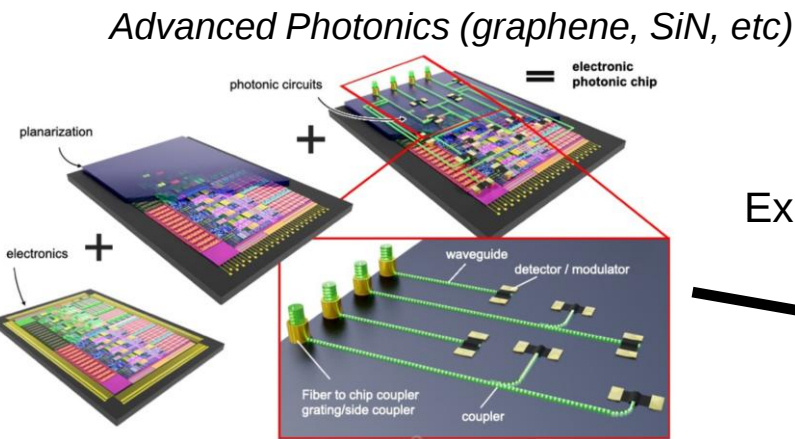


Other supporters

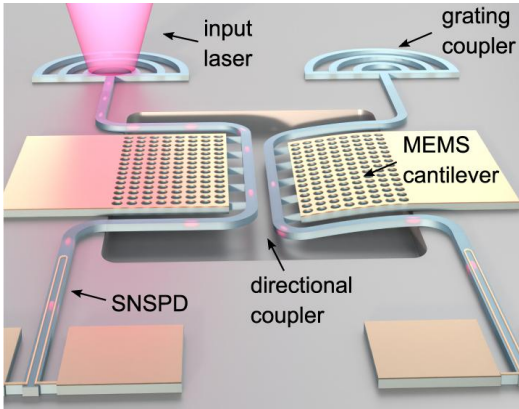


Additional supporters are being engaged

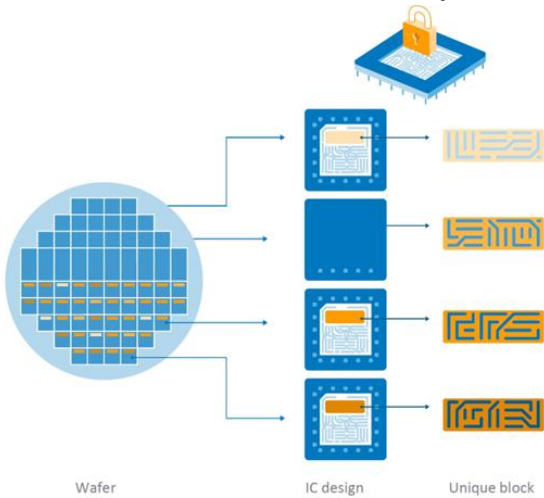
/ InnoFab’s technology roadmap: “Beyond-CMOS” & BEOL integration of advanced materials



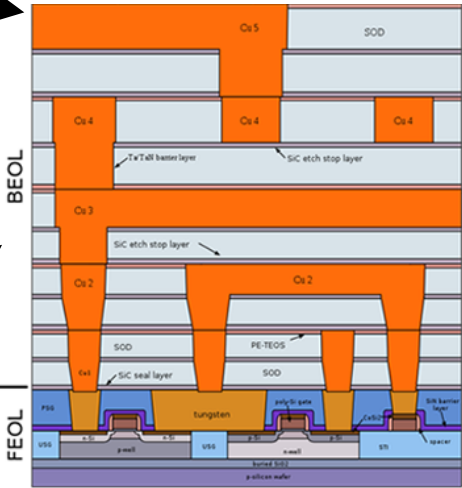
Single Photon Detector for Quantum Computers



Personalized IC’s with unique IP blocks



Examples of BEOL integrated devices



Cross-section of CMOS chip

Innovative Non-Volatile Memories

